

ABSTRACT OF THE DISCLOSURE

A display device comprises a frame buffer which time-divides a frame displaying one picture into multiple sub-frames, an attenuation signal generating circuit which generates an attenuation signal by
5 dividing the inputted luminosity signal by the designated attenuation coefficient, and a signal switching circuit which inputs luminosity signals before division to the antecedent sub-frame in the relevant frame, at the same time, inputs the above-mentioned attenuation signals after division to the subsequent sub-frame. Consequently, such a hold type display
10 device is realized as is able to control the lowering of the picture brightness, as well as, prevent a moving picture from being unclear, blurred or disordered.

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